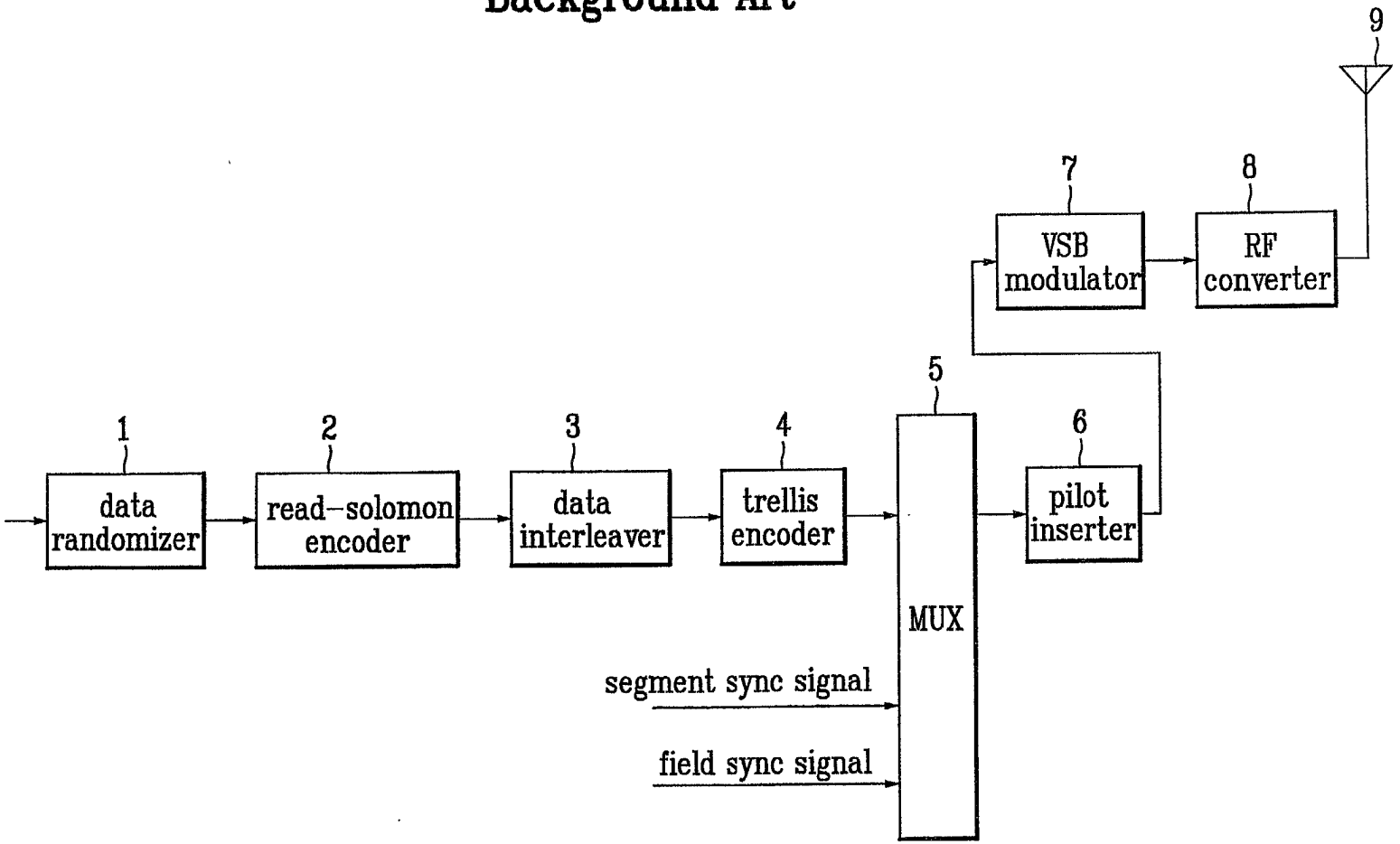
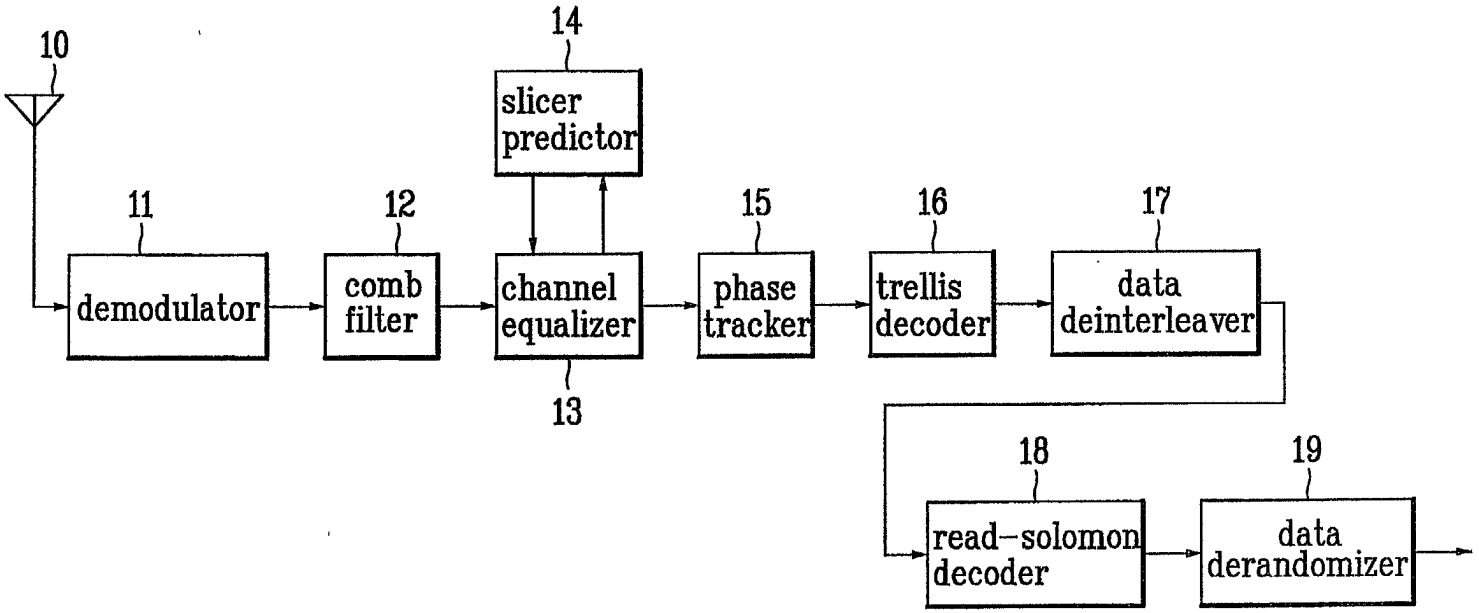
TOSTET" 6EhT660

FIG.1
Background Art



TOSHIBA SEMICONDUCTOR

FIG.2
Background Art



TOSITT" 6EHT6660

FIG.3

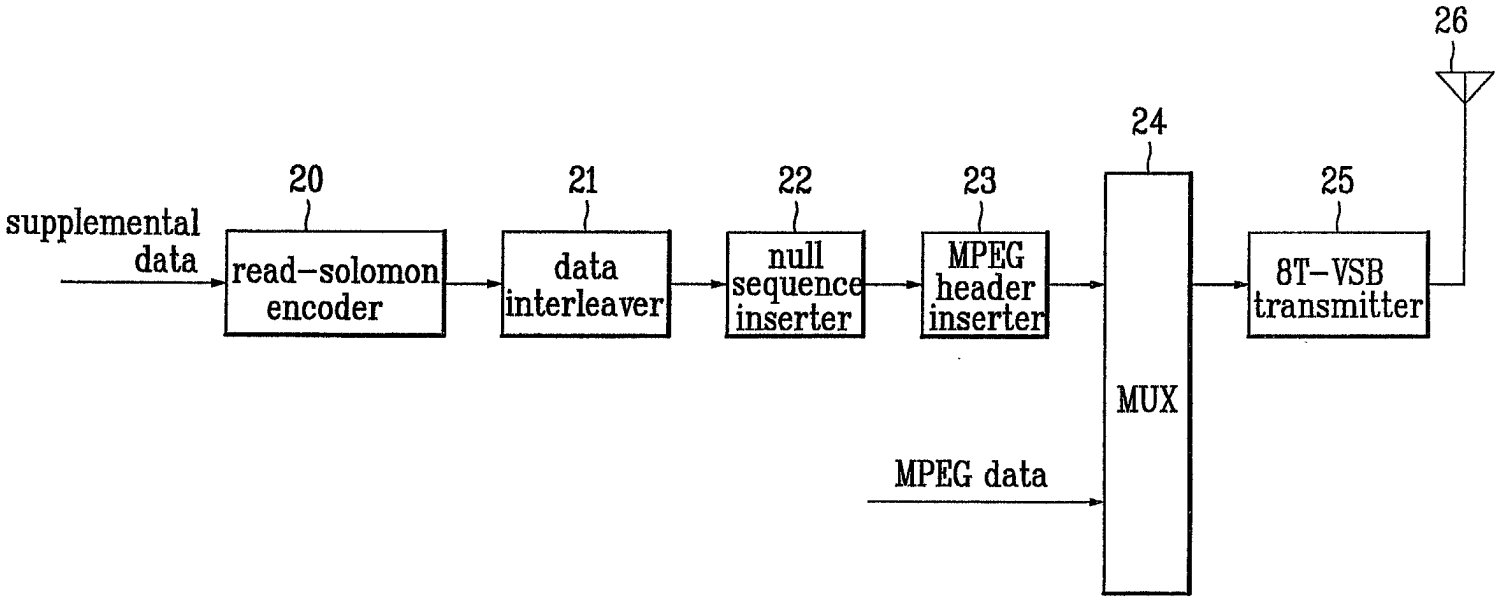
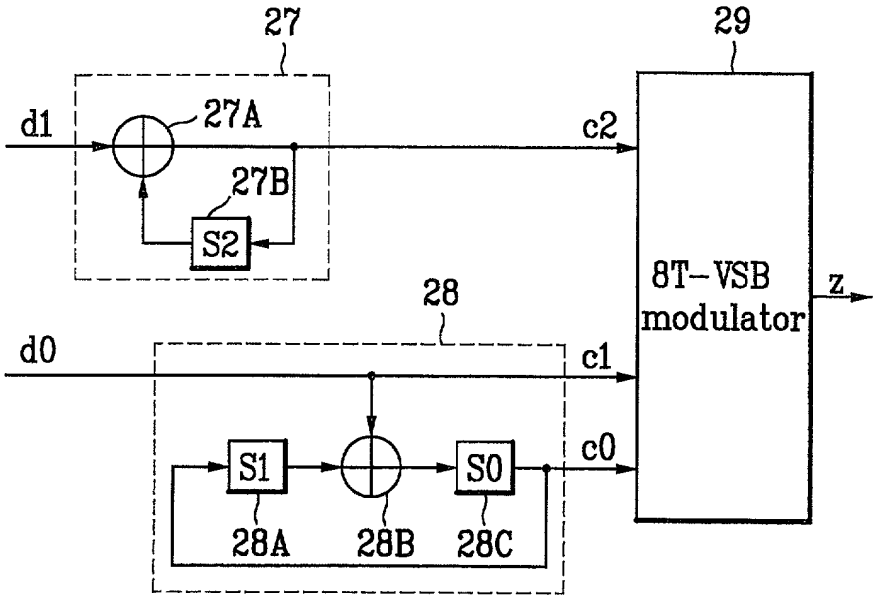


FIG. 4



c2c1c0	z
000	−7
001	−5
010	−3
011	−1
100	+1
101	+3
110	+5
111	+7

FIG.5

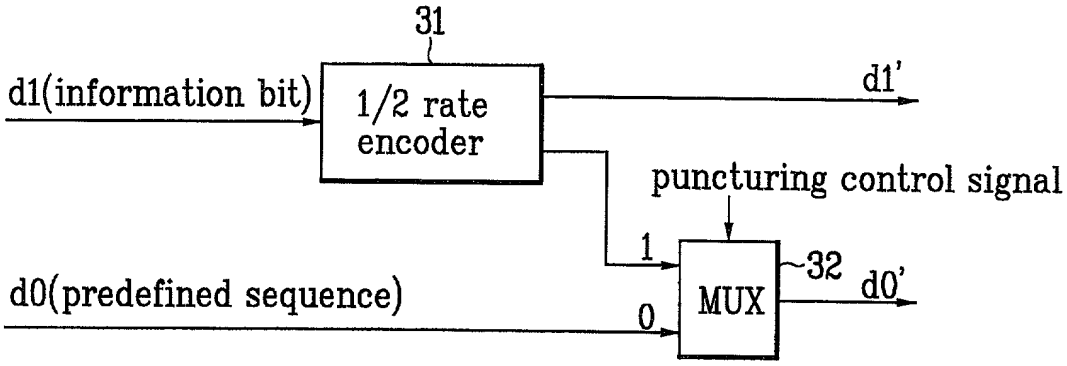
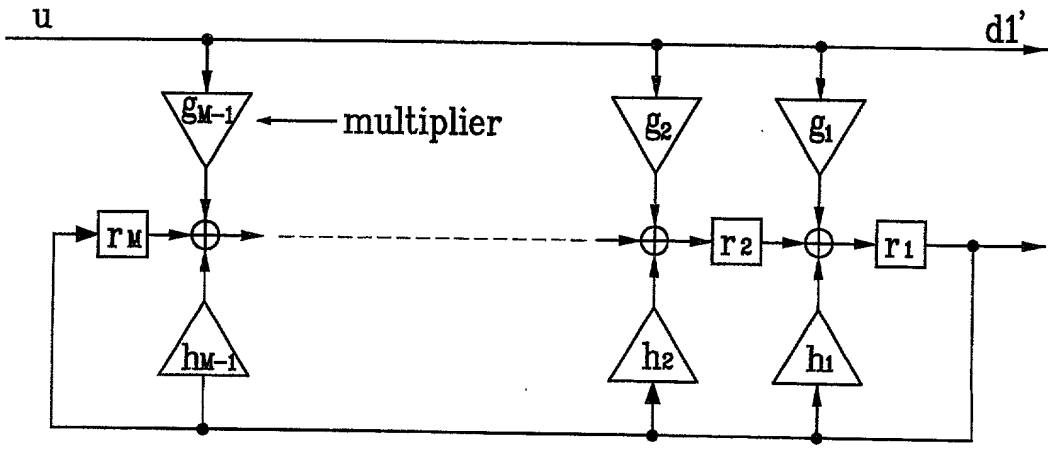
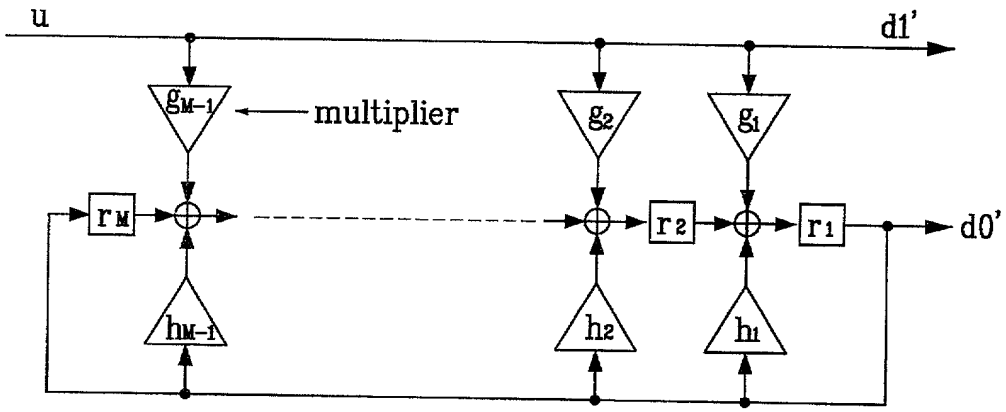


FIG.6



$g_i, h_i \in \{0,1\}, i=1 \ldots M-1$

FIG. 7



$g_i, h_i \in \{0,1\}, i=1 \ldots M-1$

FIG.8A

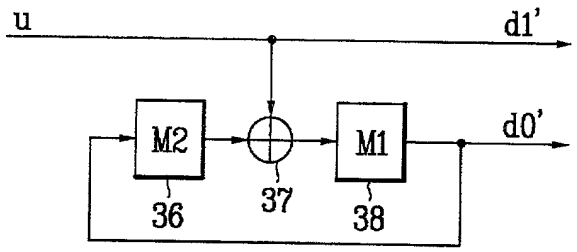


FIG.8B

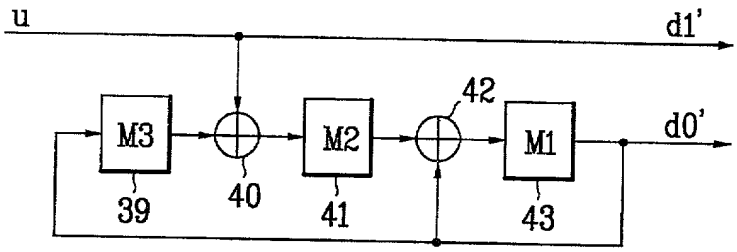


FIG.8C

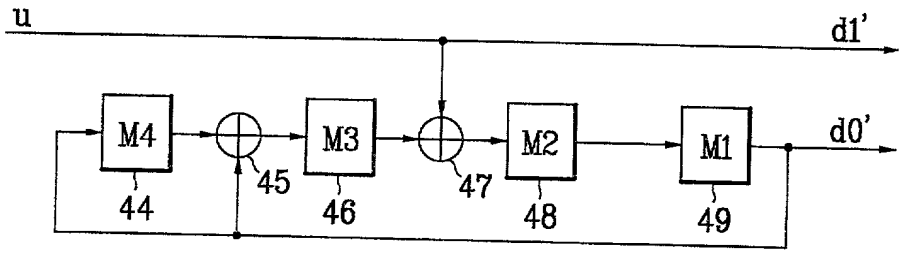


FIG. 9

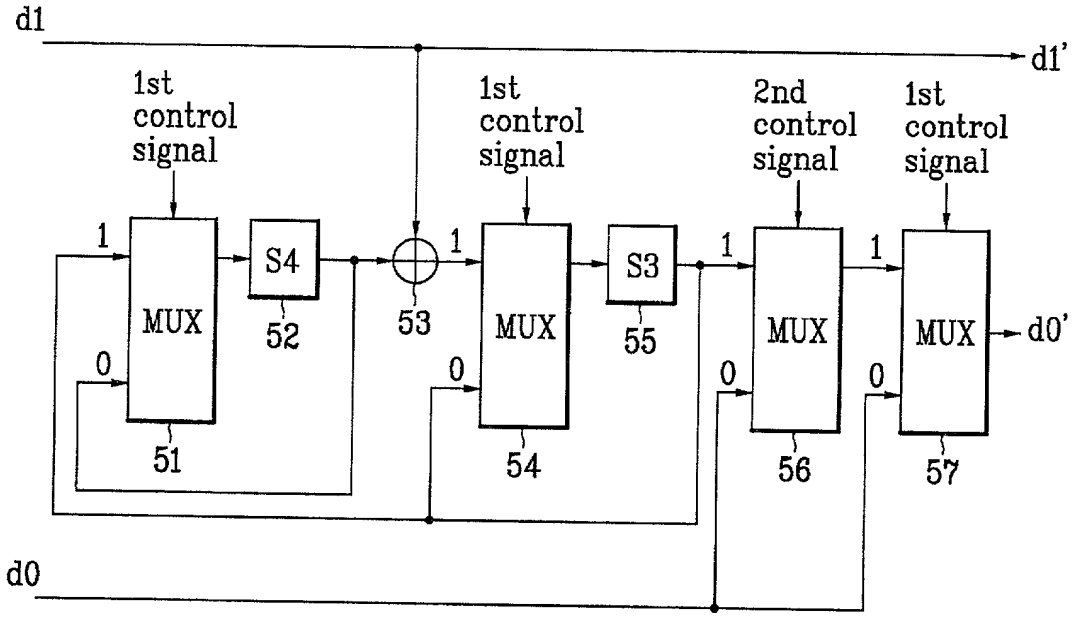

d1
d1'
1st control signal
1st control signal
2nd control signal
1st control signal
1
MUX
0
51
S4
52
53
1
MUX
0
54
S3
55
1
MUX
0
56
1
MUX
0
57
d0'
d0

FIG. 10

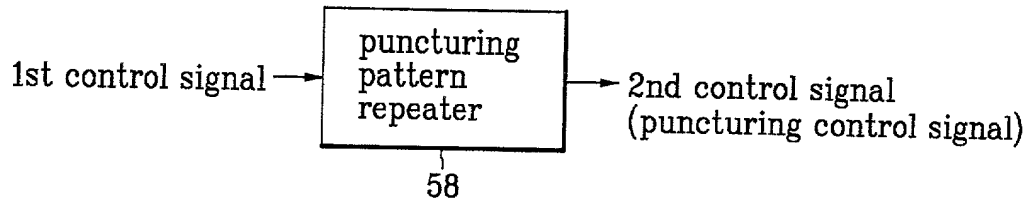
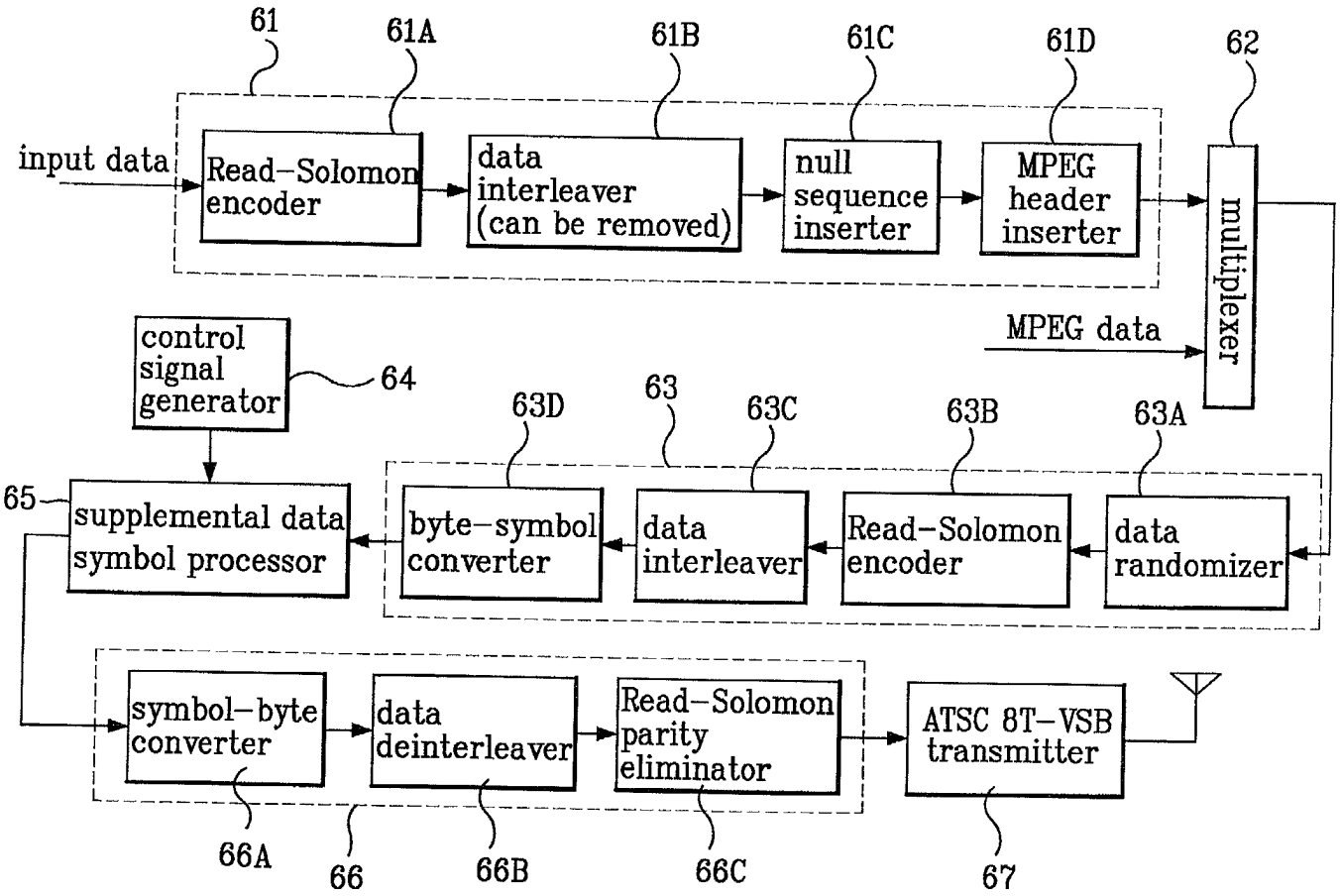

1st control signal
puncturing pattern repeater
58
2nd control signal
(puncturing control signal)

FIG.11


61
61A
61B
61C
61D
62
input data
Read-Solomon encoder
data interleaver (can be removed)
null sequence inserter
MPEG header inserter
multiplexer
MPEG data
control signal generator
64
63D
63
63C
63B
63A
65
supplemental data symbol processor
byte-symbol converter
data interleaver
Read-Solomon encoder
data randomizer
symbol-byte converter
data deinterleaver
Read-Solomon parity eliminator
ATSC 8T-VSB transmitter
66A
66
66B
66C
67